

Figure 1. Transfer curve with negative offset

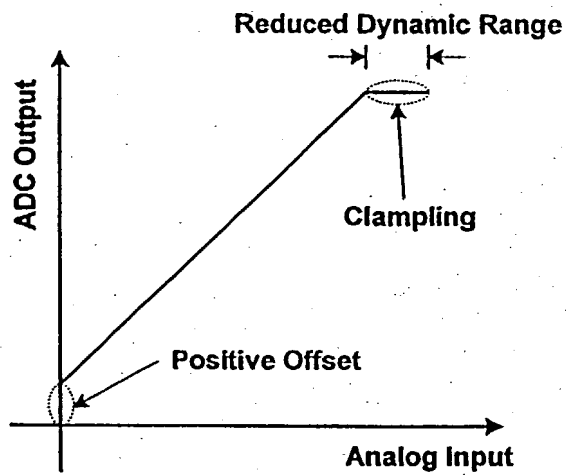


Figure 2. Transfer curve with positive offset

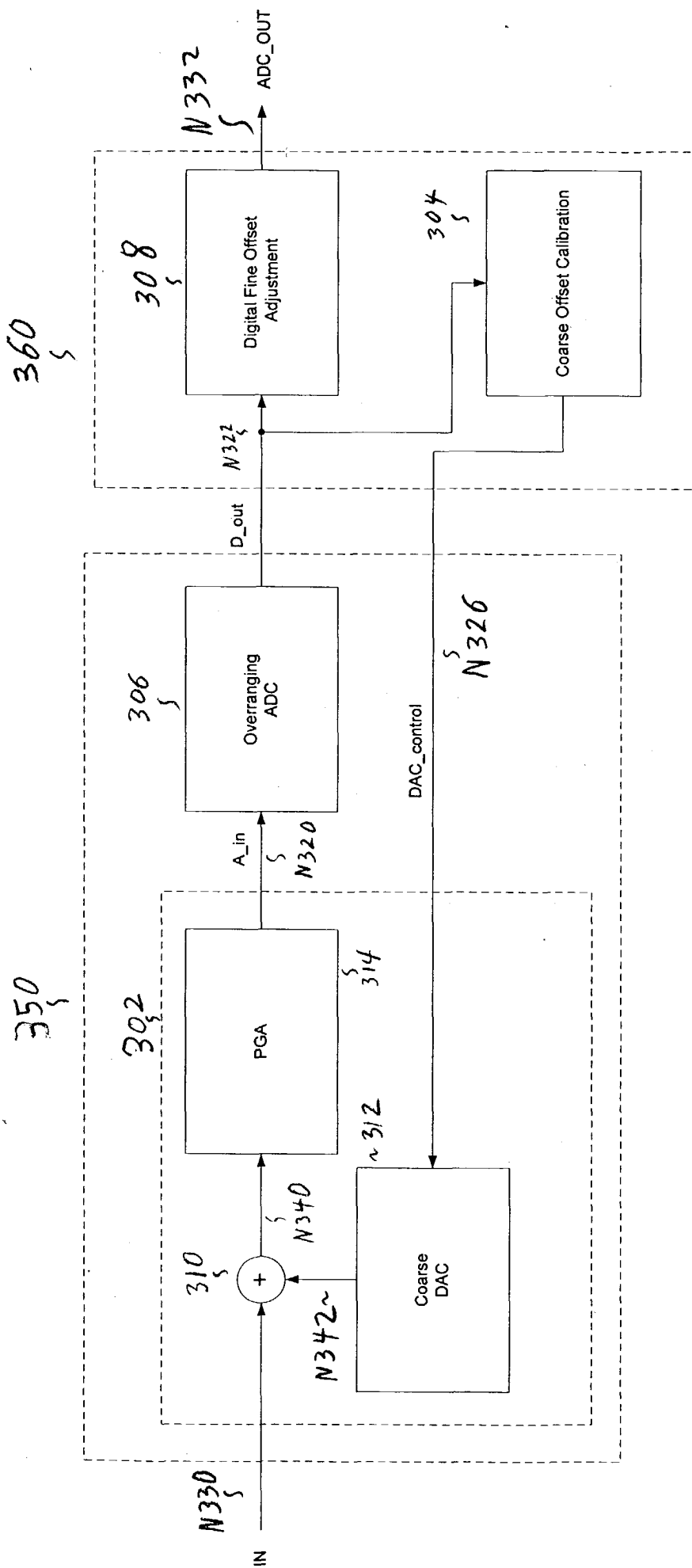


Figure 3

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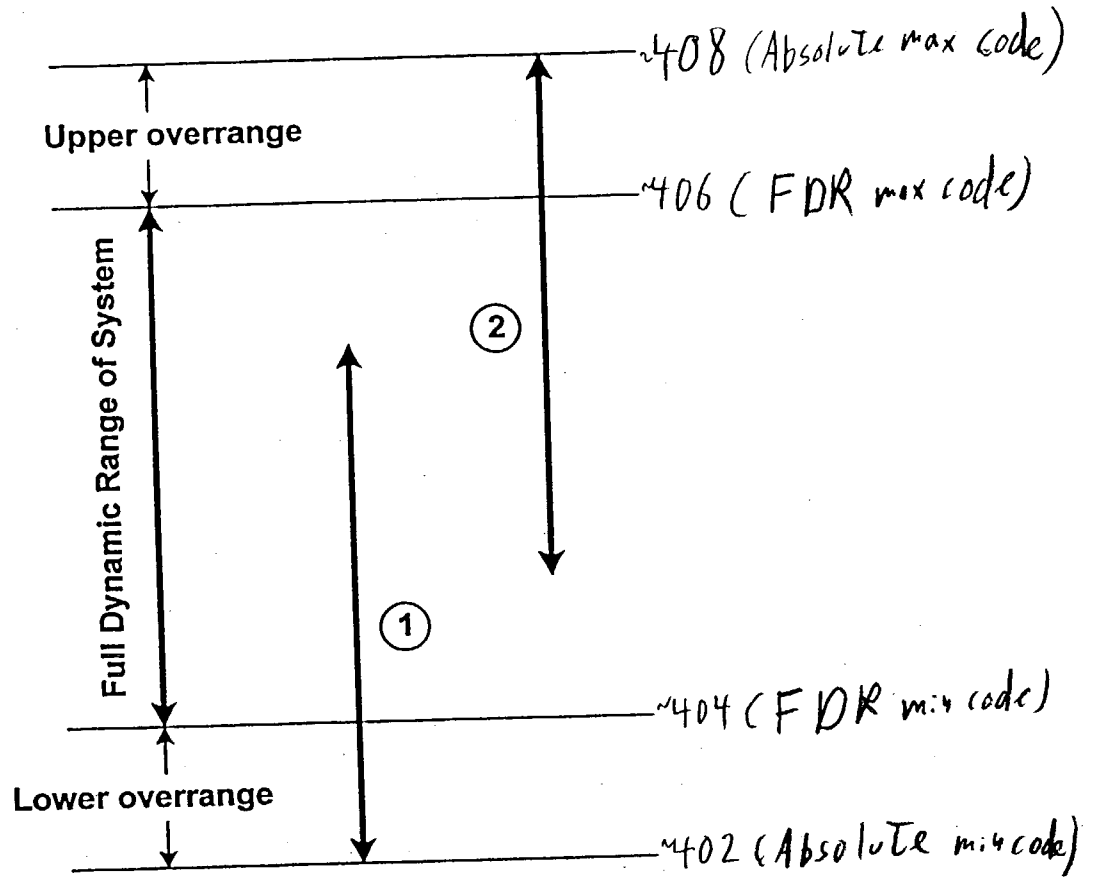


Figure 4.

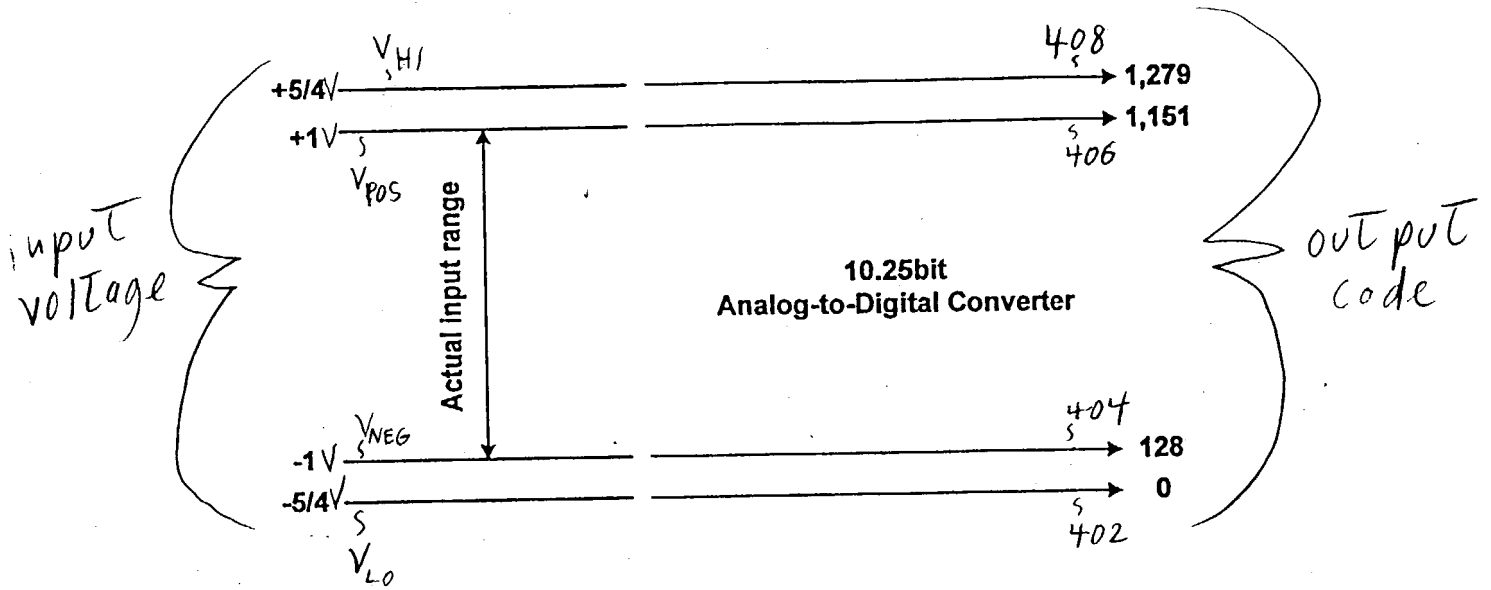
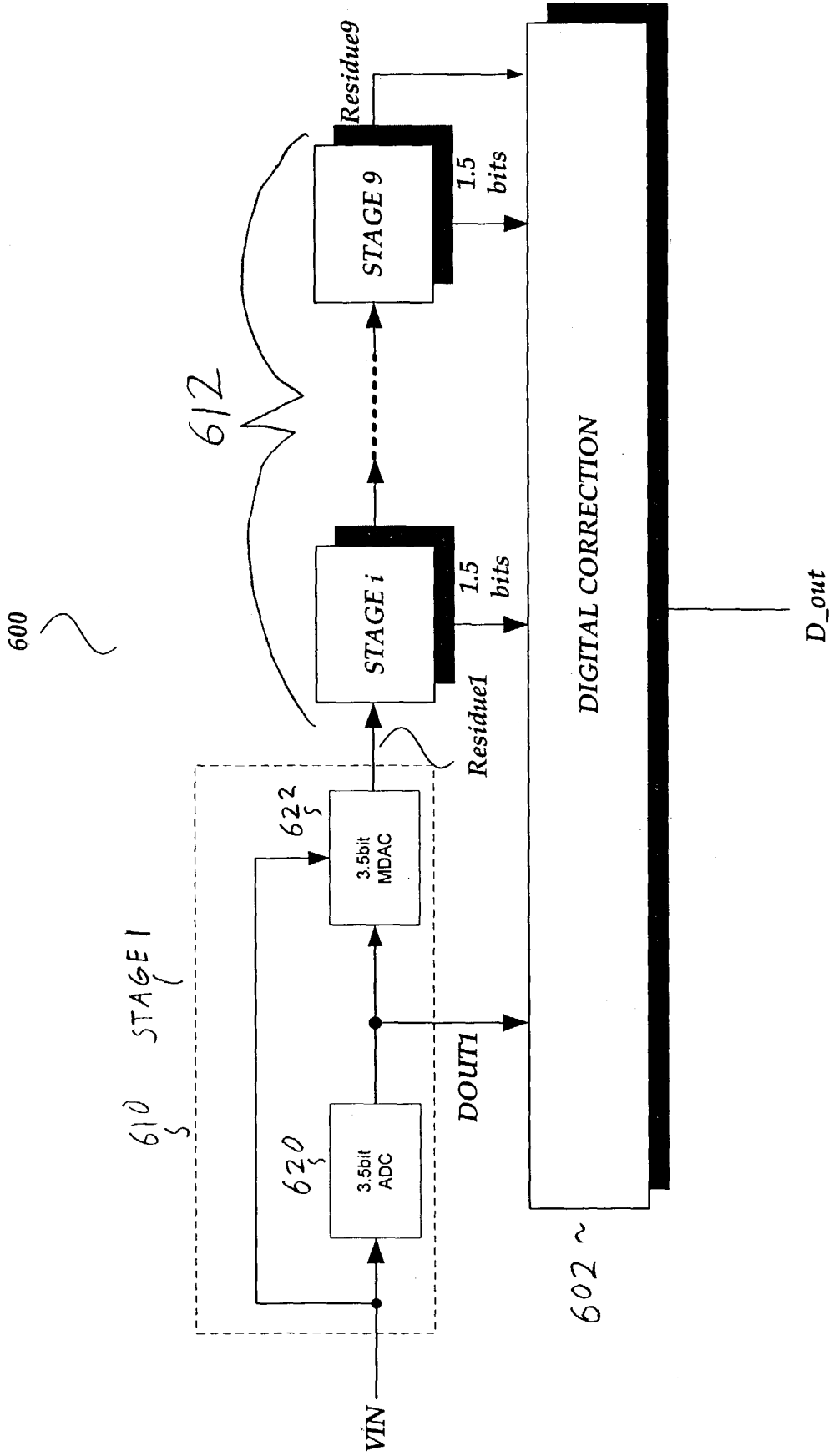


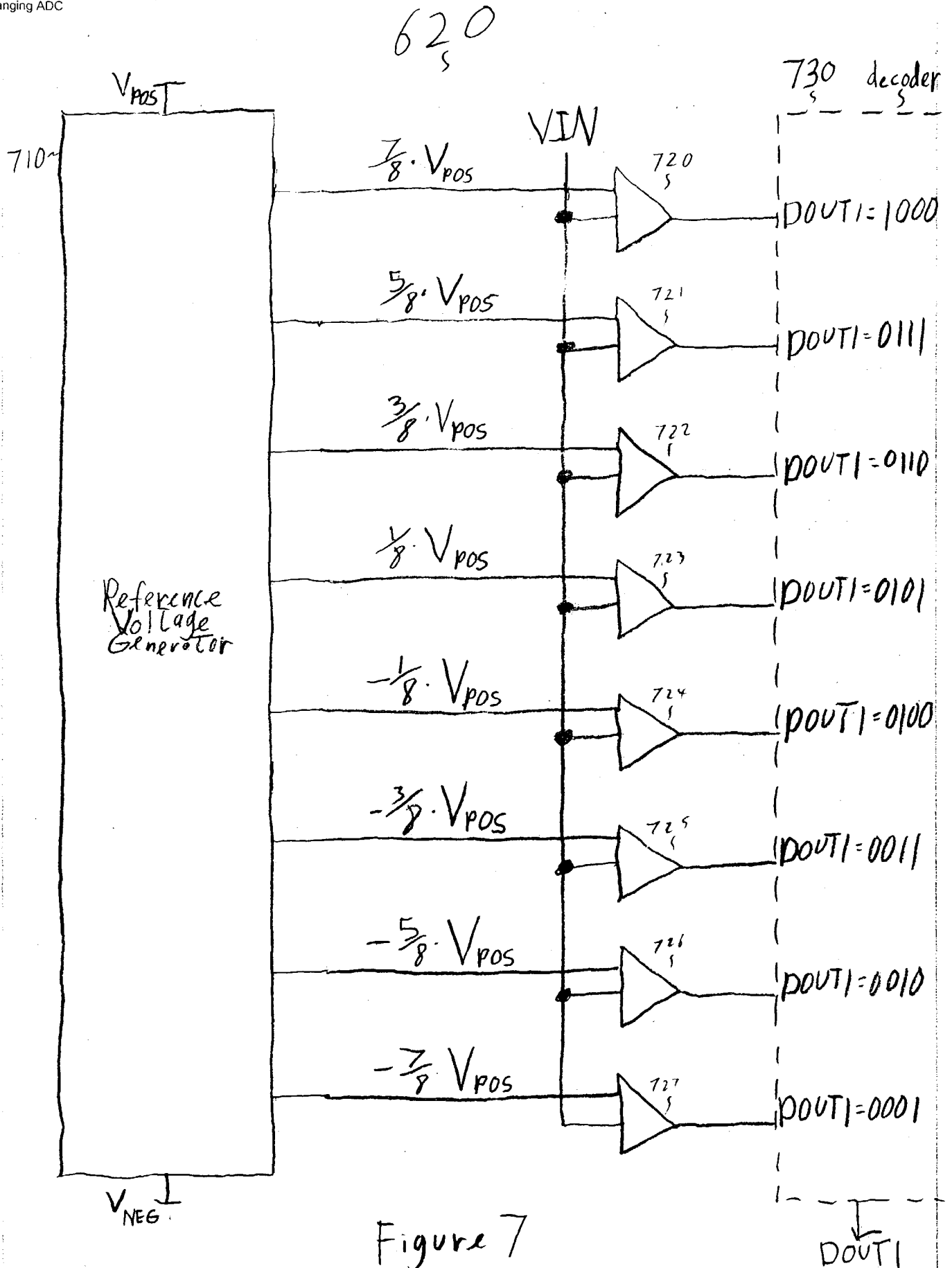
Figure 5

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Figure 6



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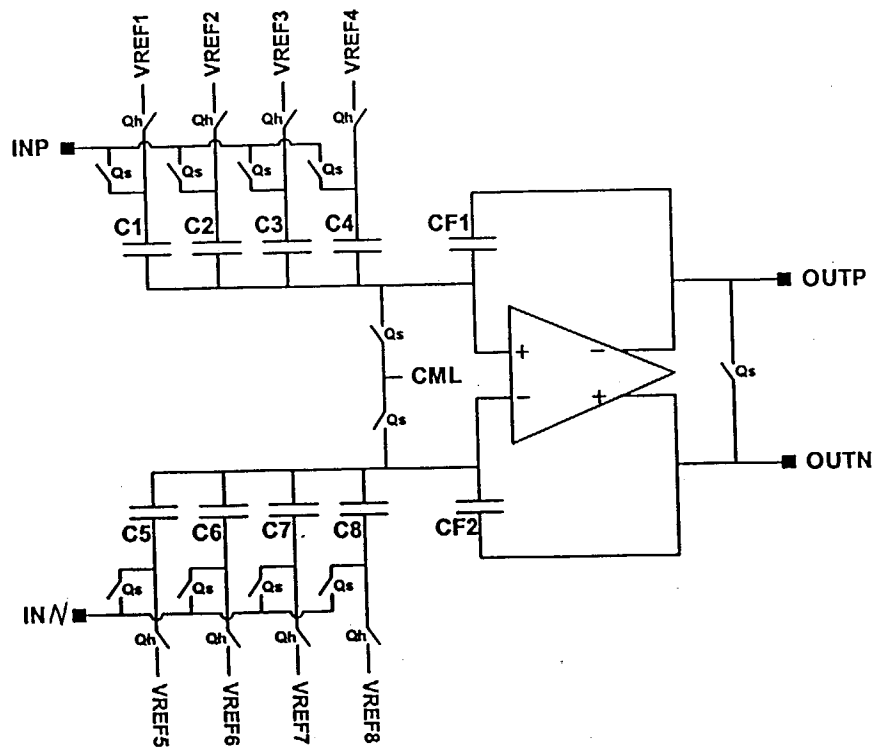


Figure 8A

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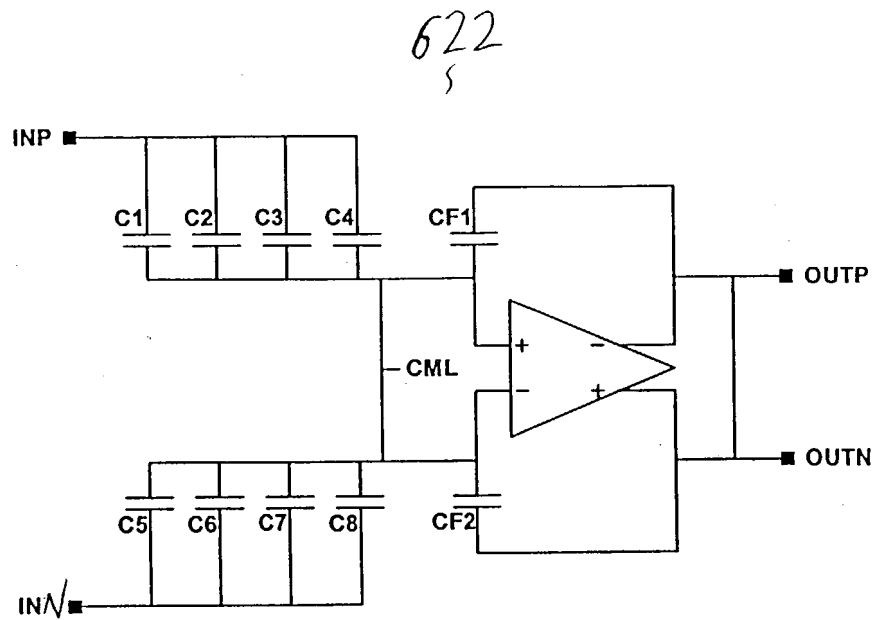


Figure 8B



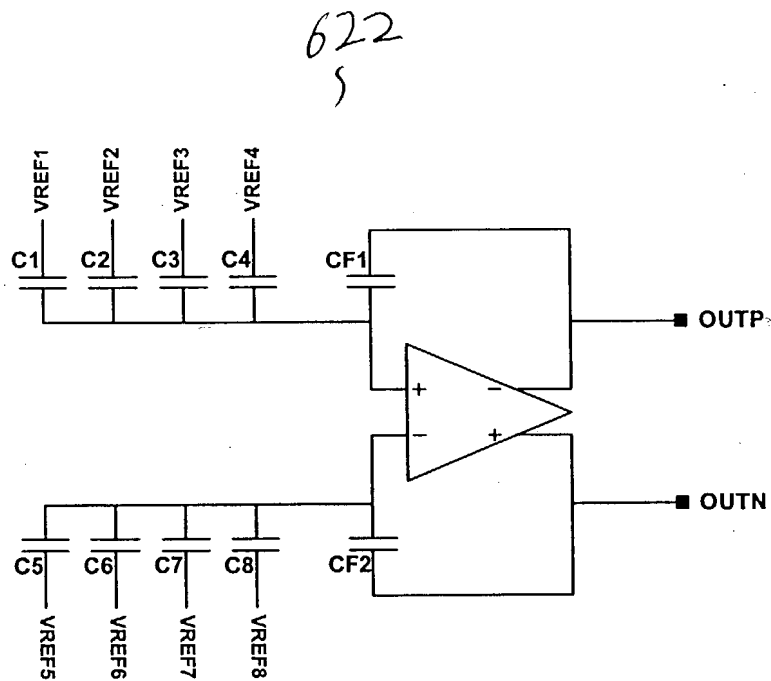
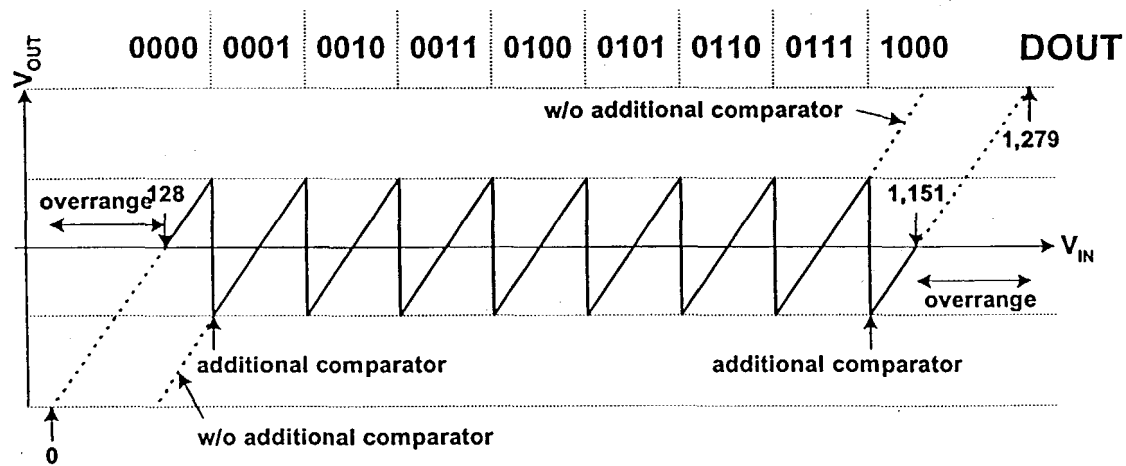


Figure 8C

	VREF1	VREF2	VREF3	VREF4	VREF5	VREF6	VREF7	VREF8	V <sub>OUT</sub>	DOVT1
$7/8 < V_{IN}$	0.5	0.5	0.5	0.5	-0.5	-0.5	-0.5	-0.5	$4V_{IN} - 4$	1000
$5/8 < V_{IN} < 7/8$	0.5	0.5	0.5	0	0	-0.5	-0.5	-0.5	$4V_{IN} - 3$	0111
$3/8 < V_{IN} < 5/8$	0.5	0.5	0	0	0	0	-0.5	-0.5	$4V_{IN} - 2$	0110
$1/8 < V_{IN} < 3/8$	0.5	0	0	0	0	0	0	-0.5	$4V_{IN} - 1$	0101
$-1/8 < V_{IN} < 1/8$	0	0	0	0	0	0	0	0	$4V_{IN}$	0100
$-1/8 < V_{IN} < -3/8$	-0.5	0	0	0	0	0	0	0.5	$4V_{IN} + 1$	0011
$-3/8 < V_{IN} < -5/8$	-0.5	-0.5	0	0	0	0	0.5	0.5	$4V_{IN} + 2$	0010
$-7/8 < V_{IN} < -5/8$	-0.5	-0.5	-0.5	0	0	0.5	0.5	0.5	$4V_{IN} + 3$	0001
$V_{IN} < -7/8$	-0.5	-0.5	-0.5	-0.5	0.5	0.5	0.5	0.5	$4V_{IN} + 4$	0000

Figure 9



Residue of 3.5bit First MDAC

Figure 10

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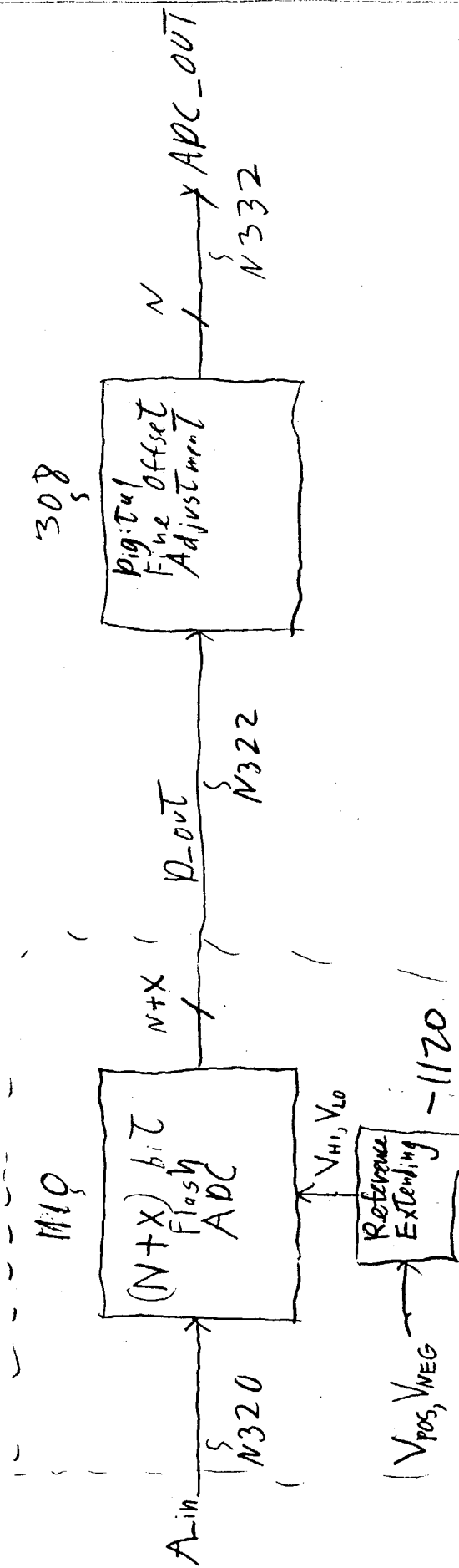


Figure 11 (Informal Draft)

306

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